IN THE CLAIMS

1. (Currently Amended) An apparatus, comprising:

a plurality of inner firewalls comprising a hardware component and/or a firmware component configured to operate within a personal computer, [said] the personal computer being configured to operate in a network of computers,

[said] the personal computer ~~including~~ comprising at least [one] two microprocessors and at least two memory components, the at least two microprocessors being located on a single microchip;

[said] at least one of the plurality of inner firewalls being configured to deny access to at least a first memory component of [said] the personal computer by another computer through a network connection with [said] the personal computer during a shared operation, and

[said] at least one of the plurality of inner firewalls being configured to allow access to at least a second memory component of [said] the personal computer by [said] the [an]other computer through [said] the network connection with [said] the personal computer during [said] the shared operation.

2. (Currently Amended) The apparatus of claim 1, wherein ~~at least one of~~ a hardware component, software file, and/or firmware file ~~has its own~~ is located within one of the inner firewalls.

3. (Currently Amended) The apparatus of claim 1, wherein at least two of a hardware component, a software file, and a firmware file is grouped exclusively together inside ~~an~~ one of the inner firewall<u>s</u>.

4. (Currently Amended) The apparatus of claim 1, wherein at least one of [said] <u>the</u> inner firewalls is substantially a hardware component.

5. (Currently Amended) The apparatus of claim 1, wherein [said] <u>the</u> personal computer ~~is configured for~~ <u>comprises</u> a dense wave division multiplexing (DWDM) network connection.

6. (Currently Amended) The apparatus of claim 1, wherein [said] <u>the</u> personal computer ~~is configured for~~ <u>comprises</u> a wireless <u>network</u> connection.

7. (Currently Amended) The apparatus of claim 6, wherein ~~said wireless connection is to said network~~ <u>the personal computer comprises a hardware encryption component</u>.

8. (Currently Amended) The apparatus of claim 1, wherein an operating system ~~includes~~ <u>comprises</u> ~~a number of~~ <u>more than one</u> independent component[s], ~~each~~ <u>one or more of the</u> component<u>s</u> having its own firewall.

9. (Currently Amended) The apparatus of claim 1, wherein ~~a part of~~ an operating system ~~includes~~ comprises ~~a number of~~ more than one independent component[s], ~~each~~ one or more of the components having its own firewall.

10. (Currently Amended) The apparatus of claim 1, wherein an application program ~~includes~~ comprises ~~a number of~~ more than one independent component[s], ~~each~~ one or more of the components having its own firewall.

11. (Currently Amended) The apparatus of claim 1, wherein ~~a part of~~ an application program ~~includes~~ comprises ~~a number of~~ more than one independent components, ~~each~~ one or more of the components having its own firewall.

12. (Currently Amended) The apparatus of claim 1, wherein all files of a network-accessible portion of volatile memory of [said] the personal computer are erased when control of [said] the network-accessible portion is transferred between [said] the network and a user of [said] the personal computer, [said] the network-accessible portion being located outside at least one of [said] the inner firewalls.

13. (Currently Amended) The apparatus of claim 12, wherein [said] the file erasure is accomplished by one of power interruption and overwriting.

14. (Currently Amended) The apparatus of claim of 1, wherein all files in a network-accessible portion of a non-volatile memory of [said] the personal computer are

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erased when control of [said] the network-accessible portion is transferred between [said] the network and a user of [said] the personal computer, said network-accessible portion being located outside at least one of [said] the inner firewalls.

15. (Currently Amended) The apparatus of claim 14, wherein said non-volatile memory is one of a magnetic random access memory (MRAM) or ovonic unified memory microchip the at least one inner firewall is located on the microchip.

16. (Currently Amended) The apparatus of claim 1, wherein [said] the first memory component includes comprises a system BIOS.

17. (Currently Amended) The apparatus of claim [1] 18, wherein said personal computer is substantially contained in a respective single the microchip comprises an encryption component.

18. (Currently Amended) The apparatus of claim 1, wherein said personal computer is substantially contained in a single respective the microchip having a plurality of at least four or 8 or 16 or 32 or 64 or 128 or 256 or 512 or 1024 microprocessors.

19. (Currently Amended) The apparatus of claim [1] 18, wherein said network of computers includes an Internet the microchip comprises a field-programmable gate array (FPGA) and/or active configuration of an integrated circuit.

20. (Currently Amended) The apparatus of claim 1, wherein [said] the network of computers ~~includes~~ comprises a World Wide Web and/or an Internet.

21. (Currently Amended) The apparatus of claim 1, wherein [said] the network connection ~~includes~~ comprises an optical fiber connection substantially directly to [said] the personal computer.

22. (Currently Amended) The apparatus of claim 1, wherein [said] the first memory component is a flash memory device.

23. (Currently Amended) The apparatus of claim 1, wherein [said] the second memory component is a flash memory device.

24. (Currently Amended) The apparatus of claim 1, wherein [said] the second memory component is a random access memory (RAM) device.

25. (Currently Amended) The apparatus of claim 1, wherein [said] the second memory component is a hard drive device.

26. (Currently Amended) The apparatus of claim 1, wherein [said] the second memory component is a read-only compact disk drive (CD-ROM) device.

27. (Currently Amended) The apparatus of claim 1, wherein [said] the second memory component is a read-only digital video disk drive (DVD) device.

28. (Currently Amended) The apparatus of claim 1, wherein [said] the second memory component is volatile memory.

29. (Currently Amended) The apparatus of claim 1, wherein [said] the second memory component is non-volatile memory.

30. (Currently Amended) The apparatus of claim 29, wherein [said] the non-volatile memory is one of comprises a magnetic random access memory (MRAM) and/or ovonic memory.

31. (Currently Amended) The apparatus of claim 1, wherein [said] the first memory component is non-volatile memory.

32. (Currently Amended) The apparatus of claim 1, wherein [said] the second memory component duplicates a first memory component.

33. (Currently Amended) The apparatus of claim 1, wherein [said] the first memory component is read and write memory.

34. (Currently Amended) The apparatus of claim 1, wherein [said] the second memory component is read-only memory.

35. (Currently Amended) An apparatus, comprising:

a plurality of inner firewalls configured to operate within a personal computer, [said] the personal computer being configured to operate in a network of computers,

[said] the personal computer including comprising at least two microprocessors,

[said] at least one of the plurality of inner firewalls being configured to deny access to at least a first microprocessor of [said] the personal computer by another computer through a network connection with [said] the personal computer during a shared operation, and

[said] at least one of the plurality of inner firewalls being configured to allow access to at least a second microprocessor of [said] the personal computer by [said] the [an]other computer through [said] the network connection with [said] the personal computer during [said] the shared operation.

36. (Currently Amended) An apparatus, comprising:

a plurality of inner firewalls configured to operate within a personal computer,

[said] the personal computer being configured to operate in a network of computers,

[said] the personal computer including comprising at least two microprocessors and at least two memory components,

[said] at least one of the plurality of inner firewalls being configured to deny

access to at least a first microprocessor and at least a first memory component of [said]

the personal computer by another computer through a network connection with [said] the

personal computer during a shared operation, and

[said] at least one of the plurality of inner firewalls being configured to allow

access to at least a second microprocessor and at least a second memory component of

[said] the personal computer by [said] the [an]other computer through [said] the network

connection with [said] the personal computer during [said] the shared operation.

37. (New) The apparatus of claim 35, wherein at least one of [said] the inner

firewalls comprises a hardware component and/or a firmware component.

38. (New) The apparatus of claim 36, wherein at least one of [said] the inner

firewalls comprises a hardware component and/or a firmware component.

39. (New) The apparatus of claim 37, wherein at least two or four or 8 or 16 or

32 or 64 or 128 or 256 or 512 or 1024 microprocessors are located on a single microchip.

40. (New) The apparatus of claim 38, wherein at least two or four or 8 or 16 or

32 or 64 or 128 or 256 or 512 or 1024 microprocessors are located on a single microchip.

41. (New) The apparatus of claim 39, wherein the microchip comprises an

encryption component.

42. (New) The apparatus of claim 40, wherein the microchip comprises an encryption component.

43. (New) The apparatus of claim 39, wherein the microchip comprises a field-programmable gate array (FPGA) and/or active configuration of an integrated circuit.

44. (New) The apparatus of claim 40, wherein the microchip comprises a field-programmable gate array (FPGA) and/or active configuration of an integrated circuit.

45. (New) The apparatus of claim 39, wherein the microchip comprises a master control and/or processing microprocessor.

46. (New) The apparatus of claim 40, wherein the microchip comprises a master control and/or processing microprocessor.

47. (New) The apparatus of claim 18, wherein the microchip comprises a master control and/or processing microprocessor.

48. (New) The apparatus of claim 39, wherein the personal computer comprises a wireless network connection.

49. (New) The apparatus of claim 40, wherein the personal computer comprises a wireless network connection.